Clock generator: Integer-N division from 1.72-2.25GHz VCO

Default device ID = 0xB0

Note: loop filter subject to adjustment
Rules for connecting (or not connecting) to analog inputs:

1. If VCMO is supported, it is okay to use DC coupling as typ case.
2. If AC coupling is used, VCMO must be grounded on the probe side.
3. If one channel is AC coupled, and an input pair is unused, tie VCMO to ground, and tie unused inputs to an AC ground (e.g. capacitors to ground).
4. If one channel is DC coupled, and an input pair is unused, tie unused inputs to VCMO directly as typ case.

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ID & cal record EEPROM, 4x8

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Patents: Apache 2.0
LA digital input comparators
0-5V range
adjustable threshold
100 MHz peak recommended speed
1M input impedance

Note: these are not for termination, they are for over-voltage protection.